

FIG. 1  
(PRIOR ART)

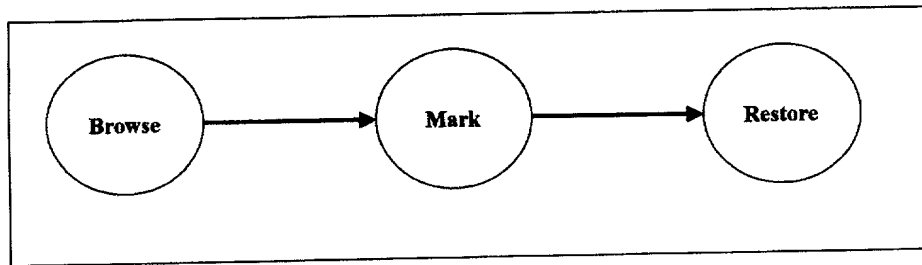


FIG 2  
(PRIOR ART)

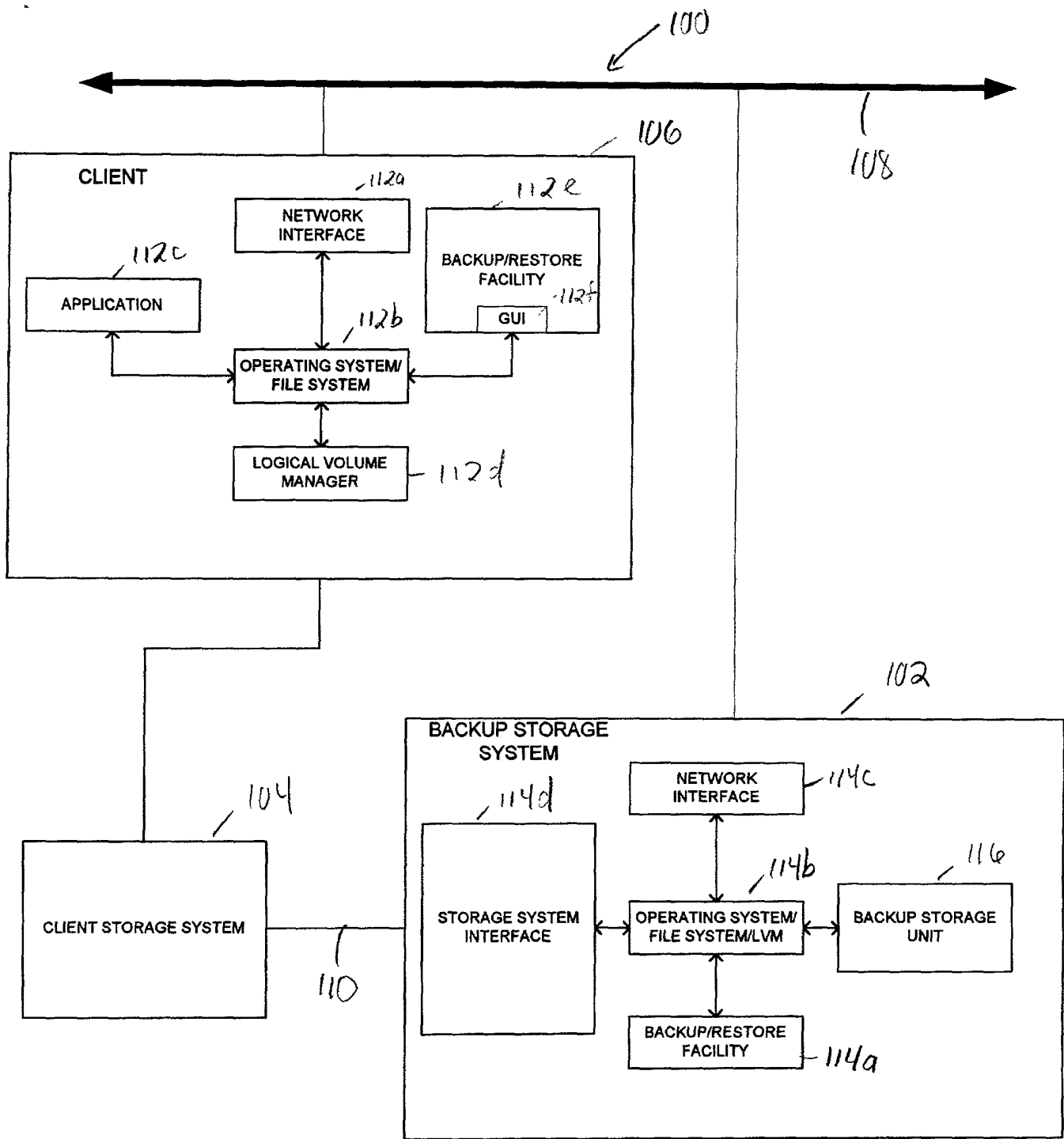


FIG 3

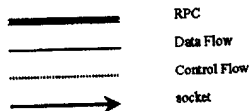
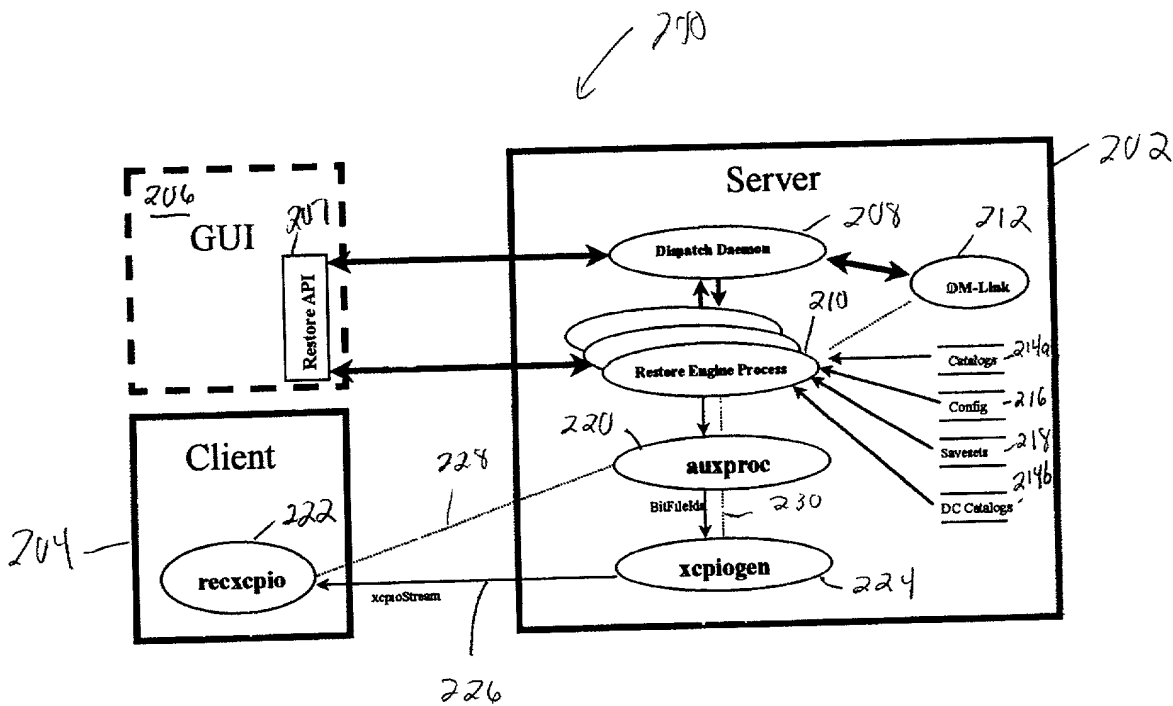


FIG 4

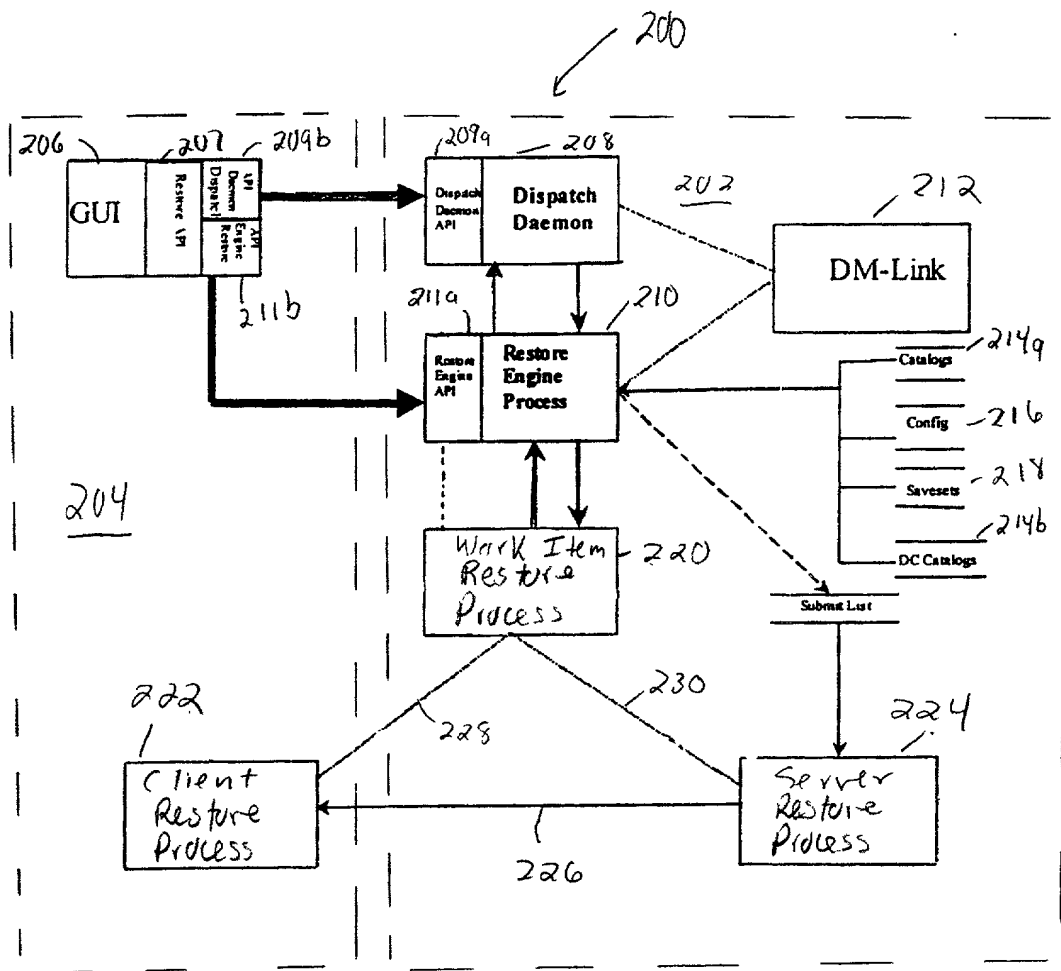


FIG 5

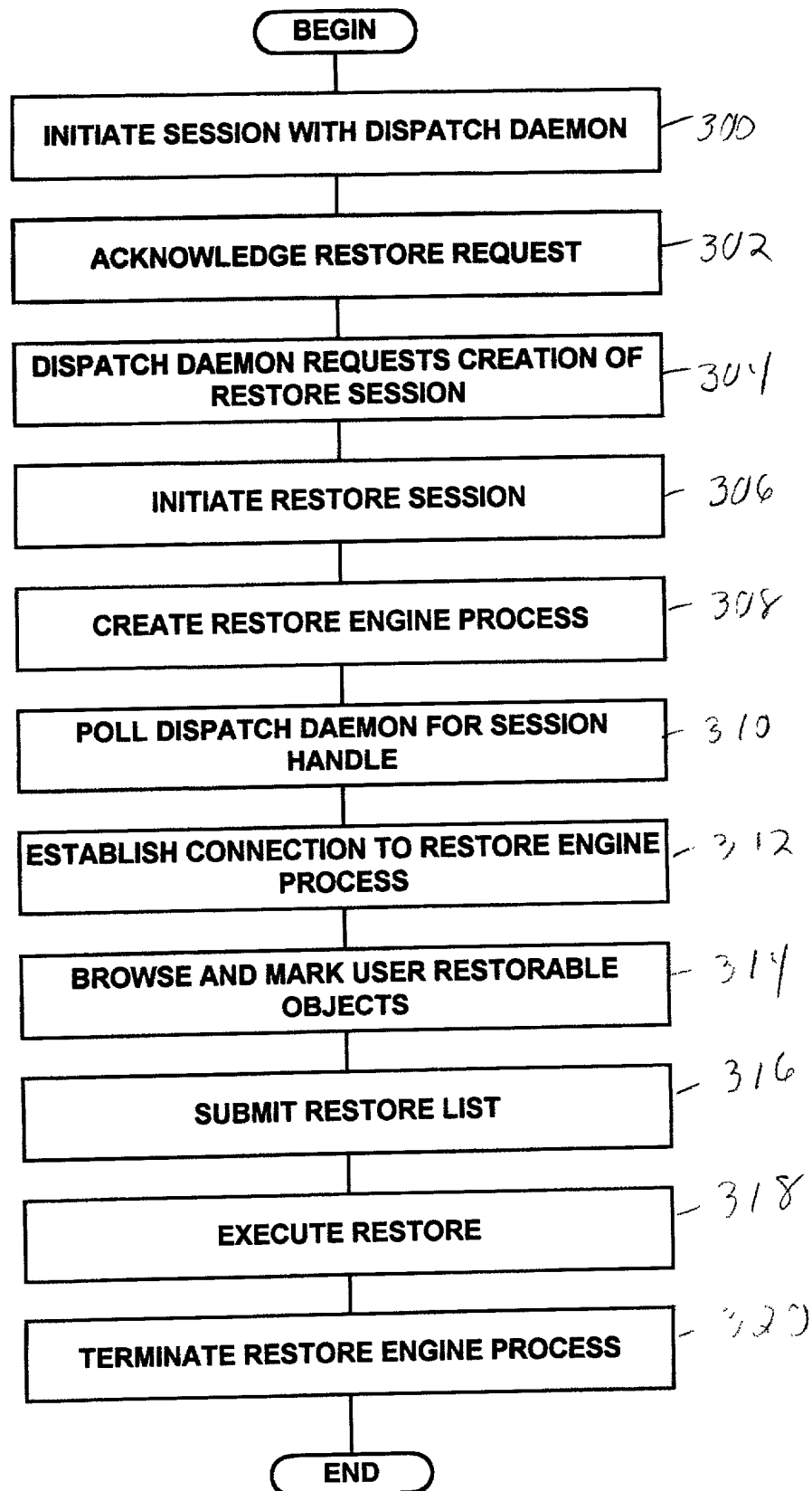


FIG. 6

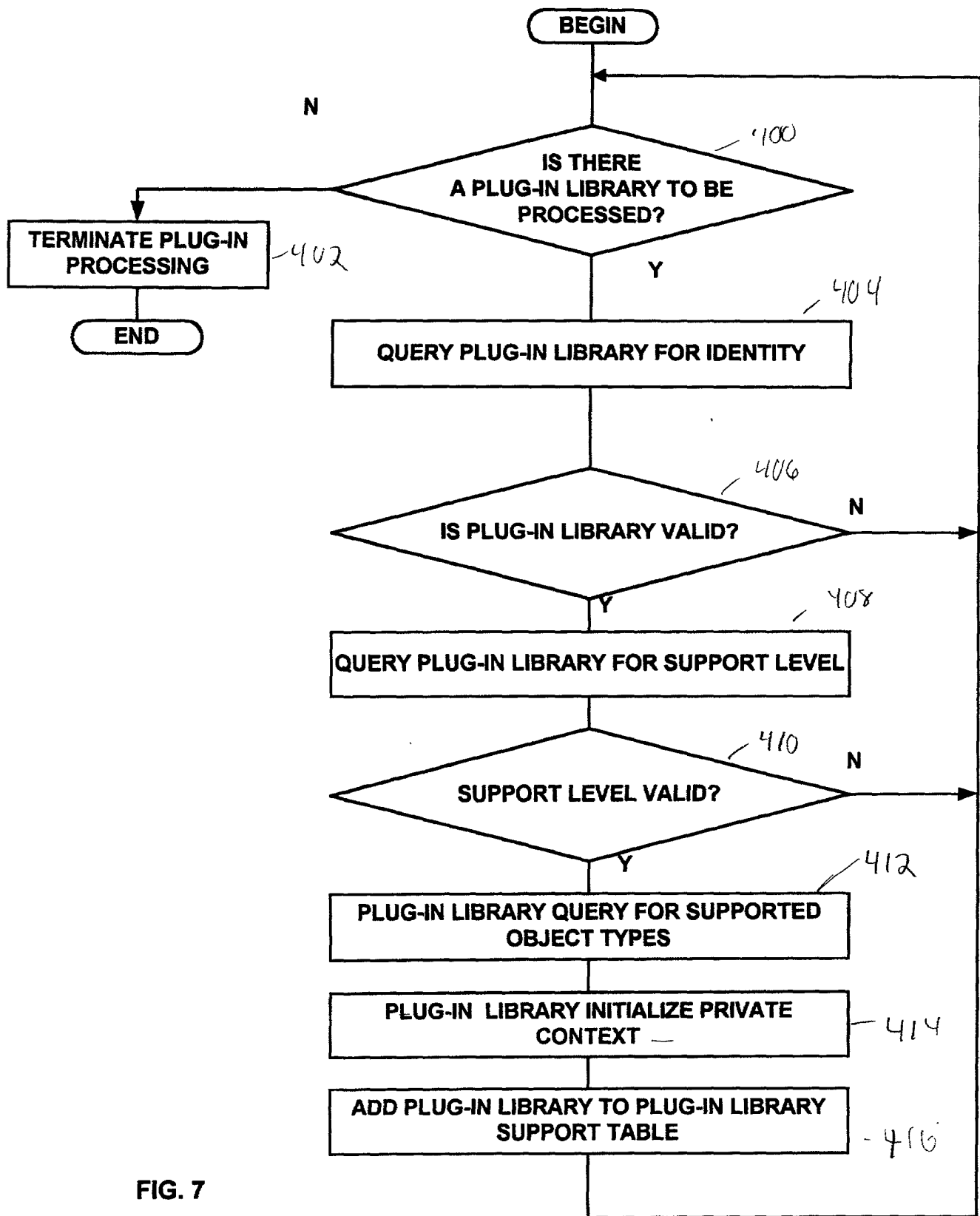


FIG. 7

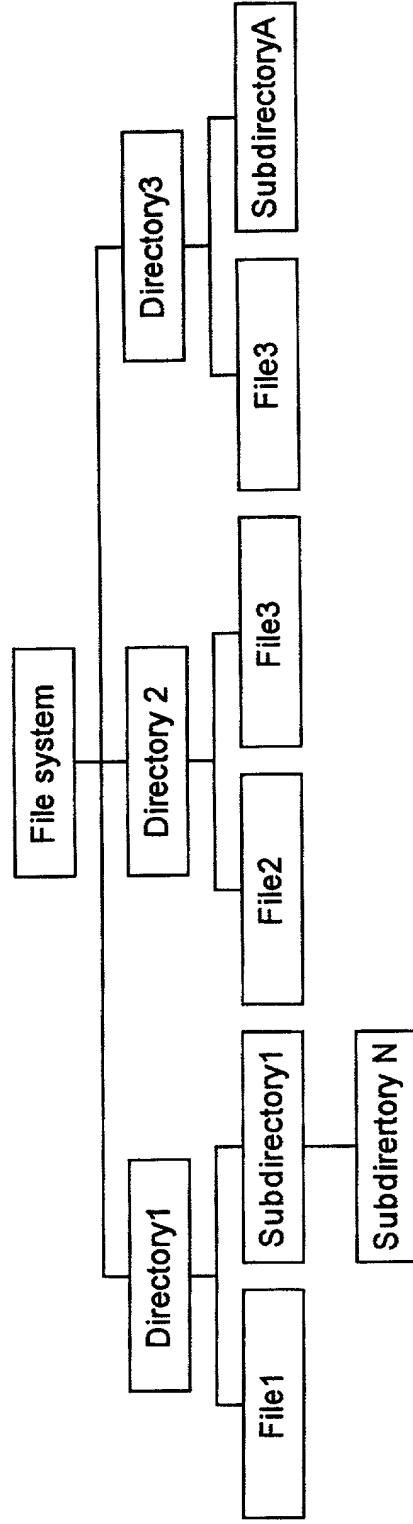
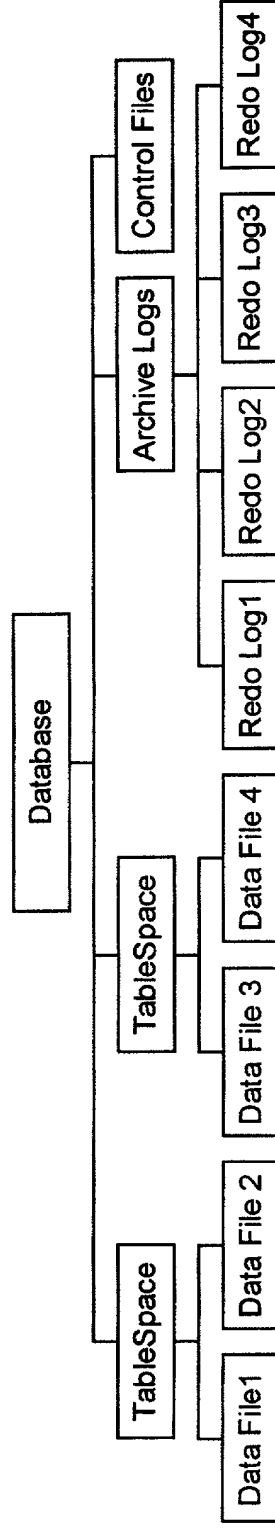


FIG. 7A



Figure 10-1: Database File Structure



K/GA. 7B

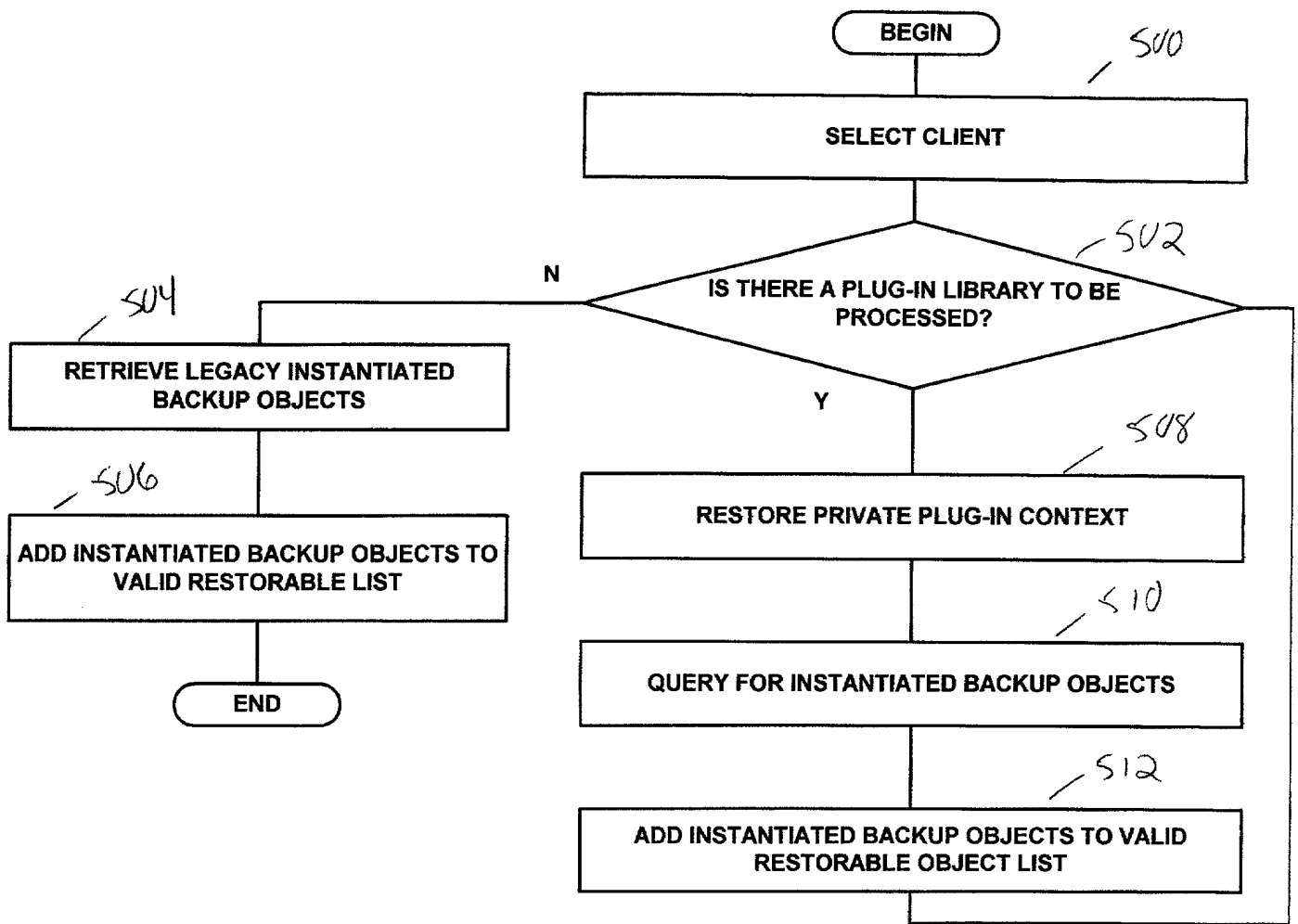


FIG 8

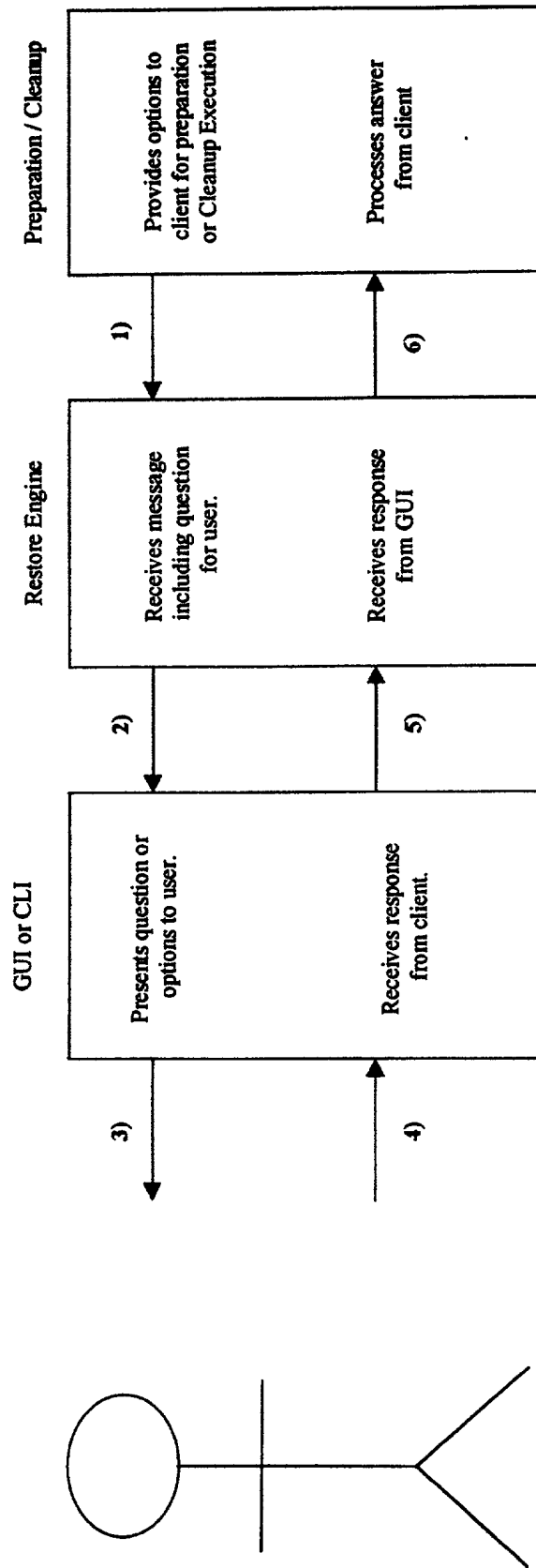


FIG. 8A

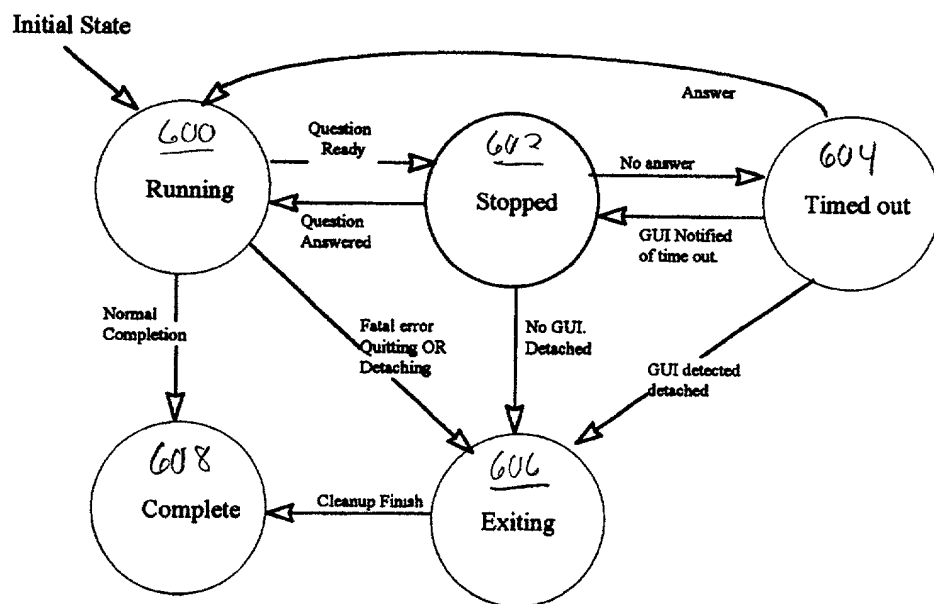


FIG 9

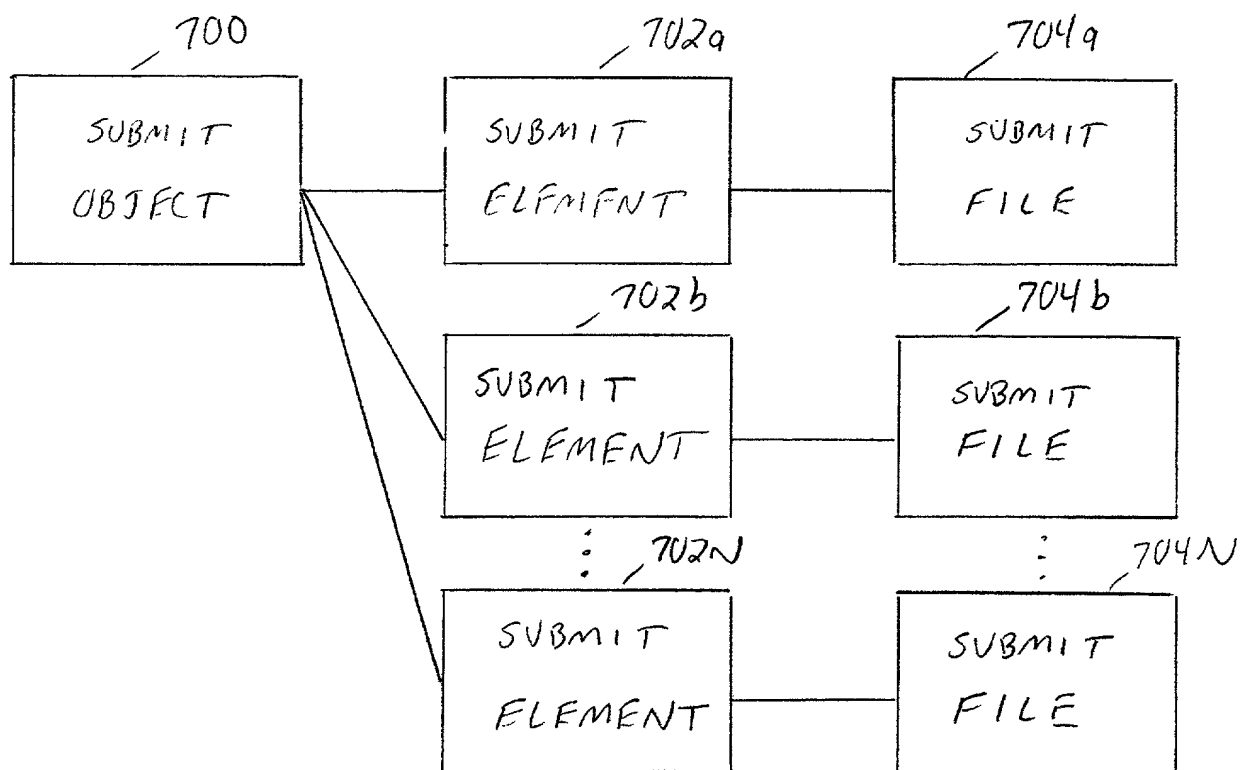


FIG. 10

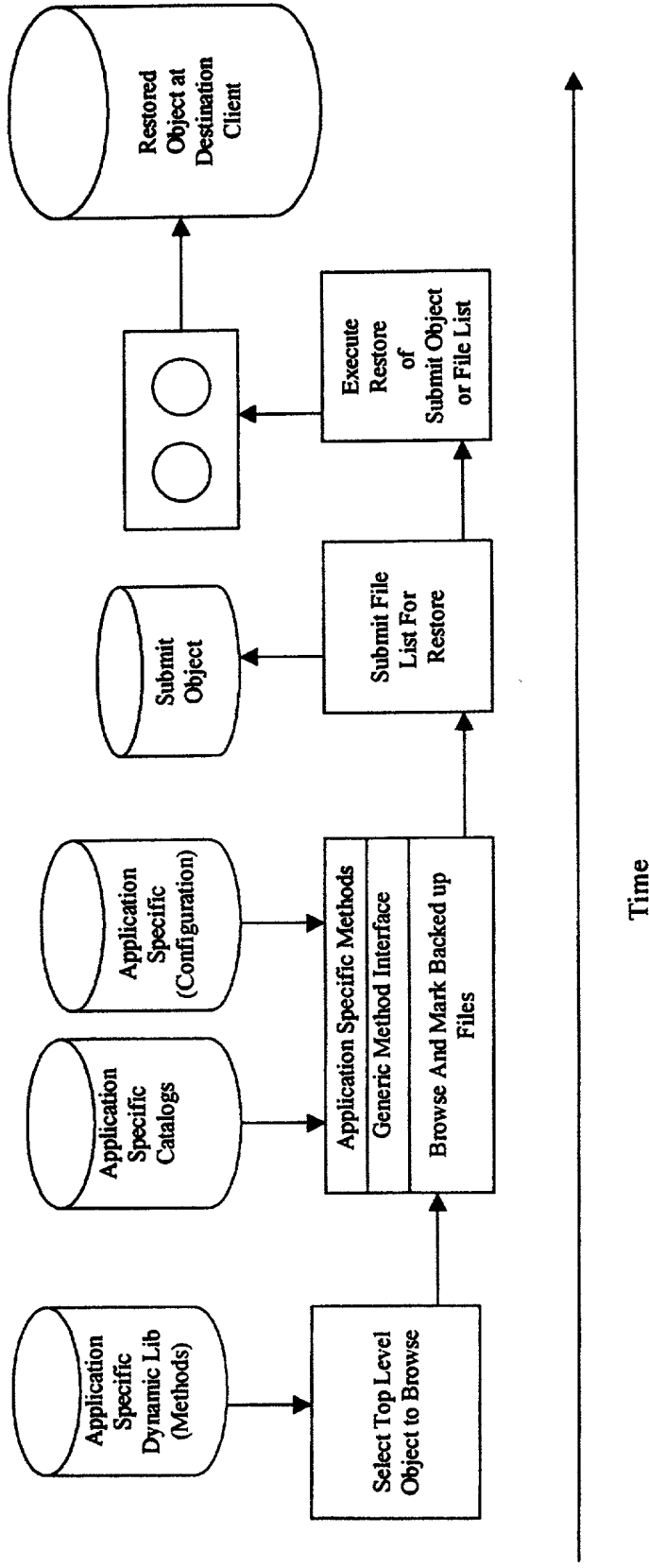


FIG. 10A

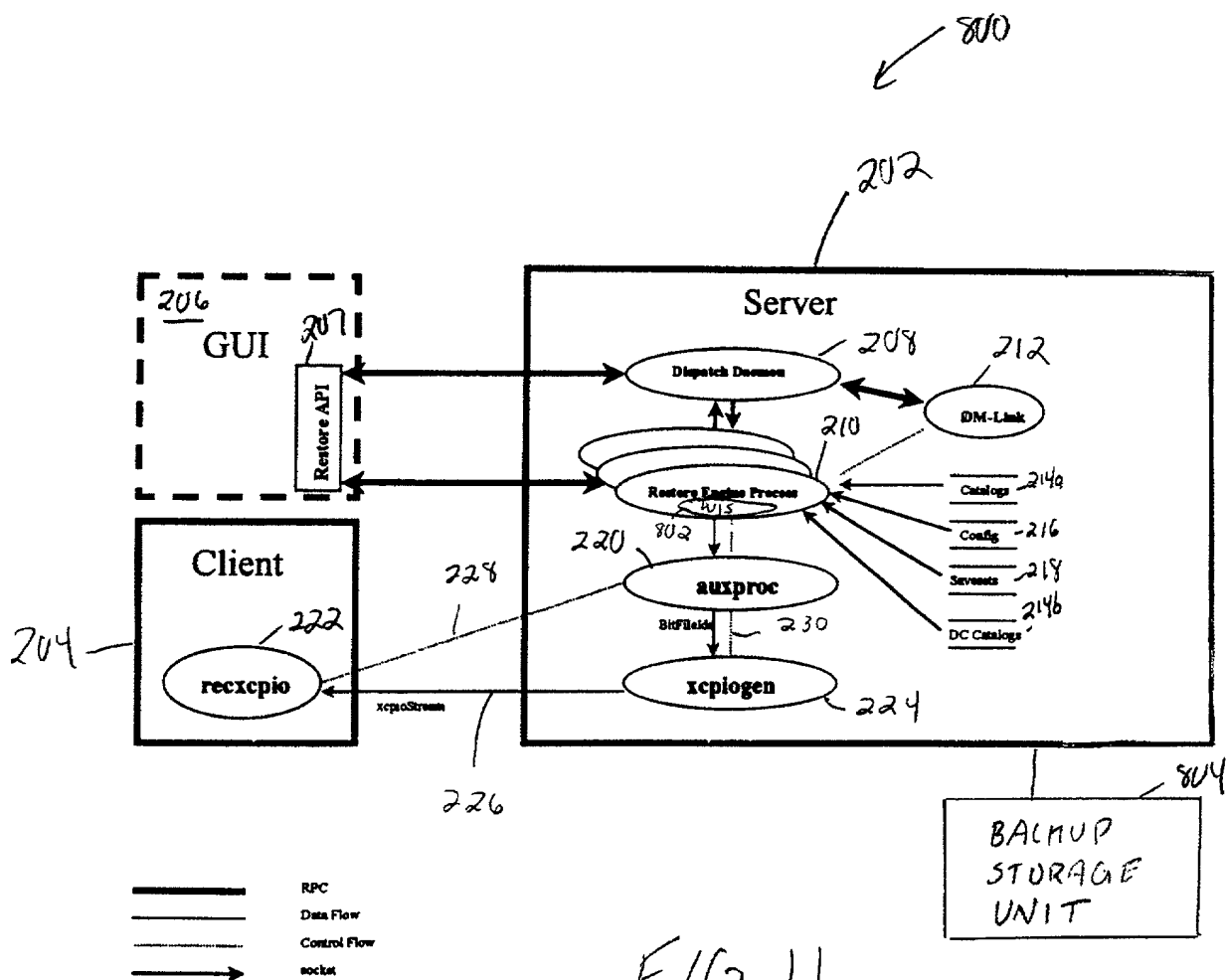


FIG 11

FIG. 12 is a block diagram of a system 804, including a first processing unit V1, a second processing unit V2, and a third processing unit V3, each including two processing elements (circles). Arrows indicate data flow from V1 to V2, and from V2 to V3.

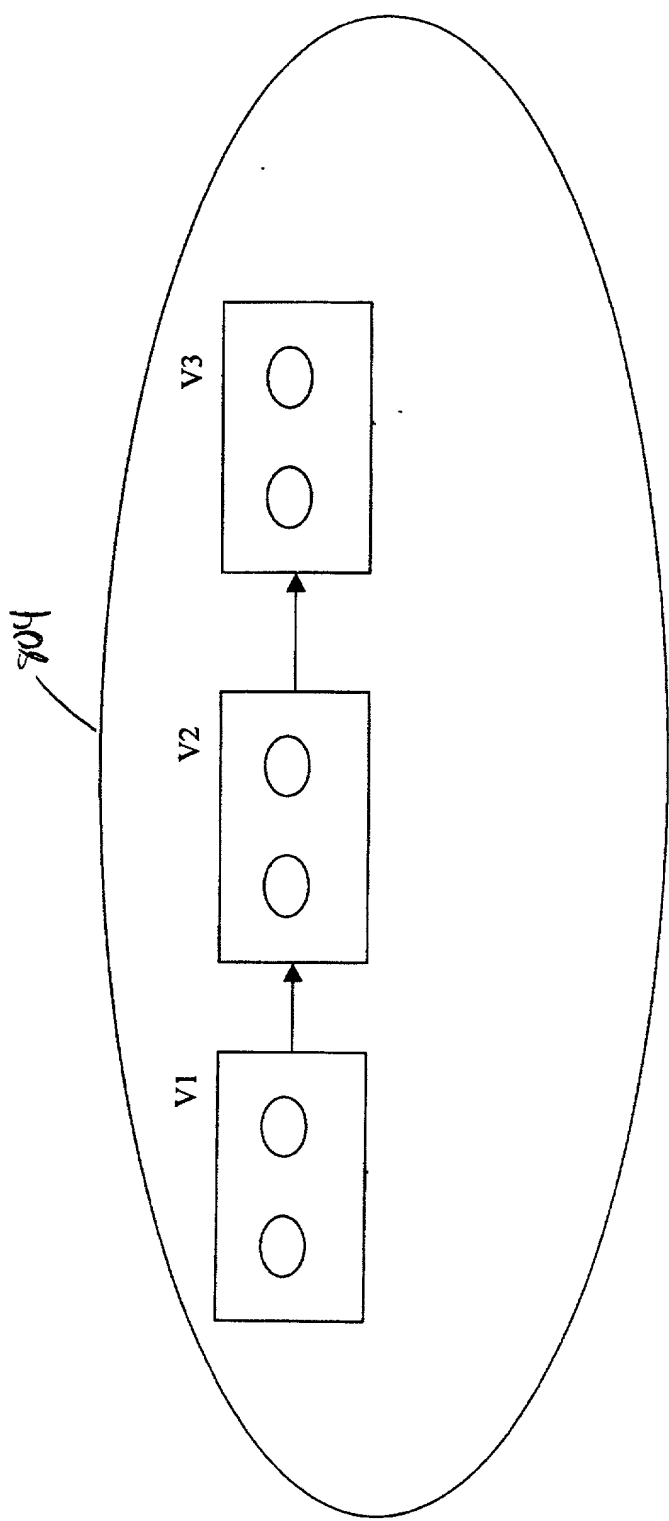
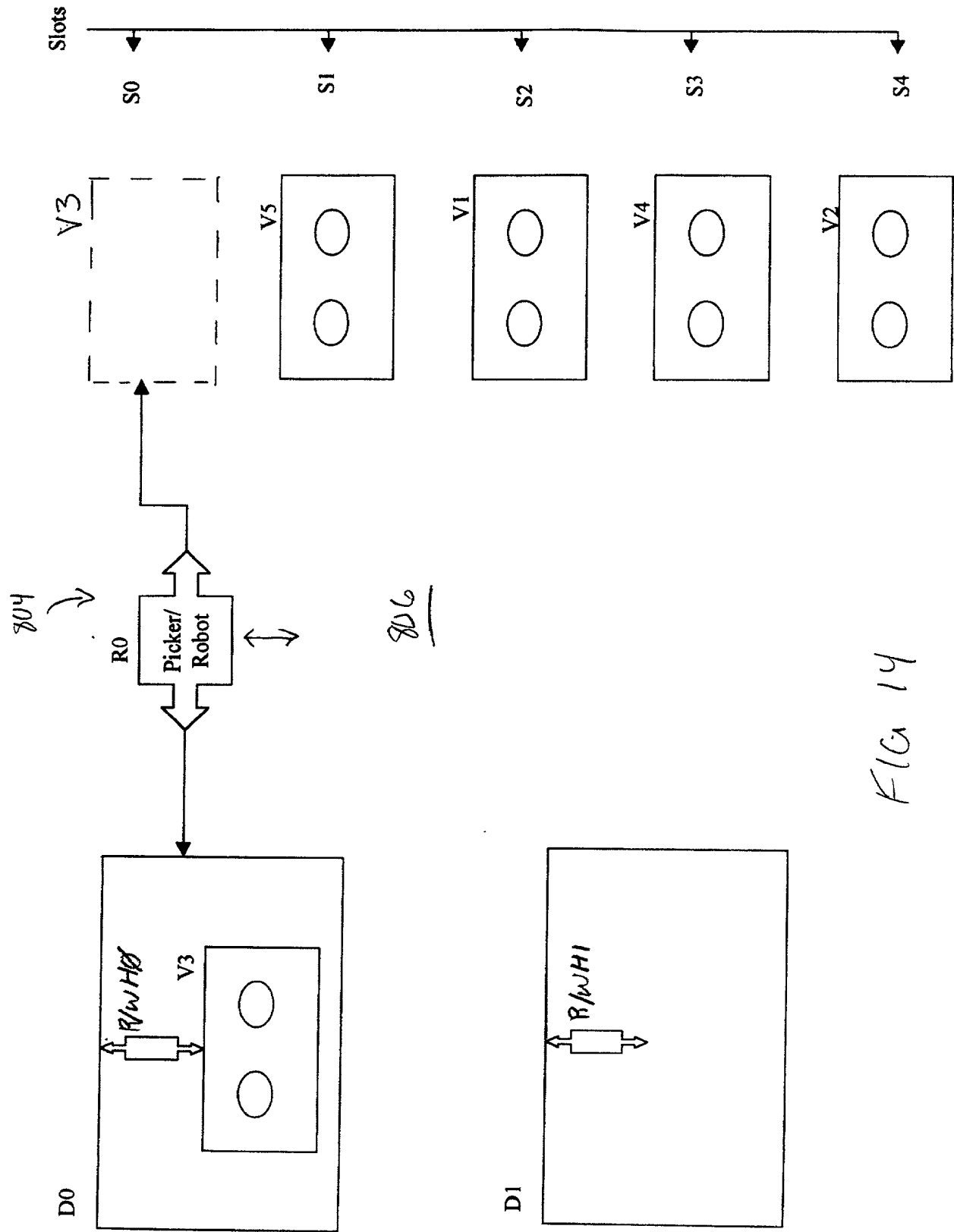


FIG. 12







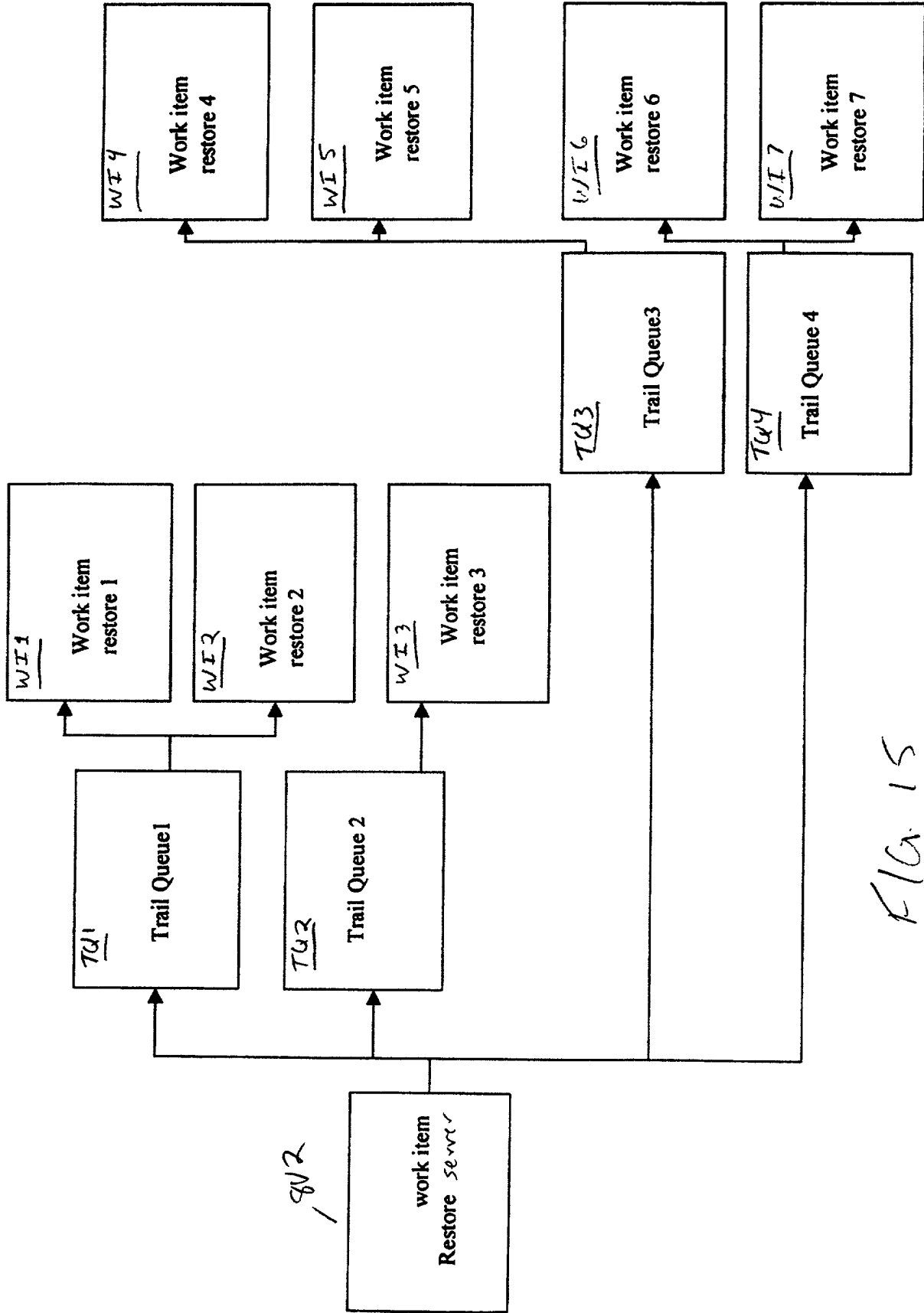


FIG. 15

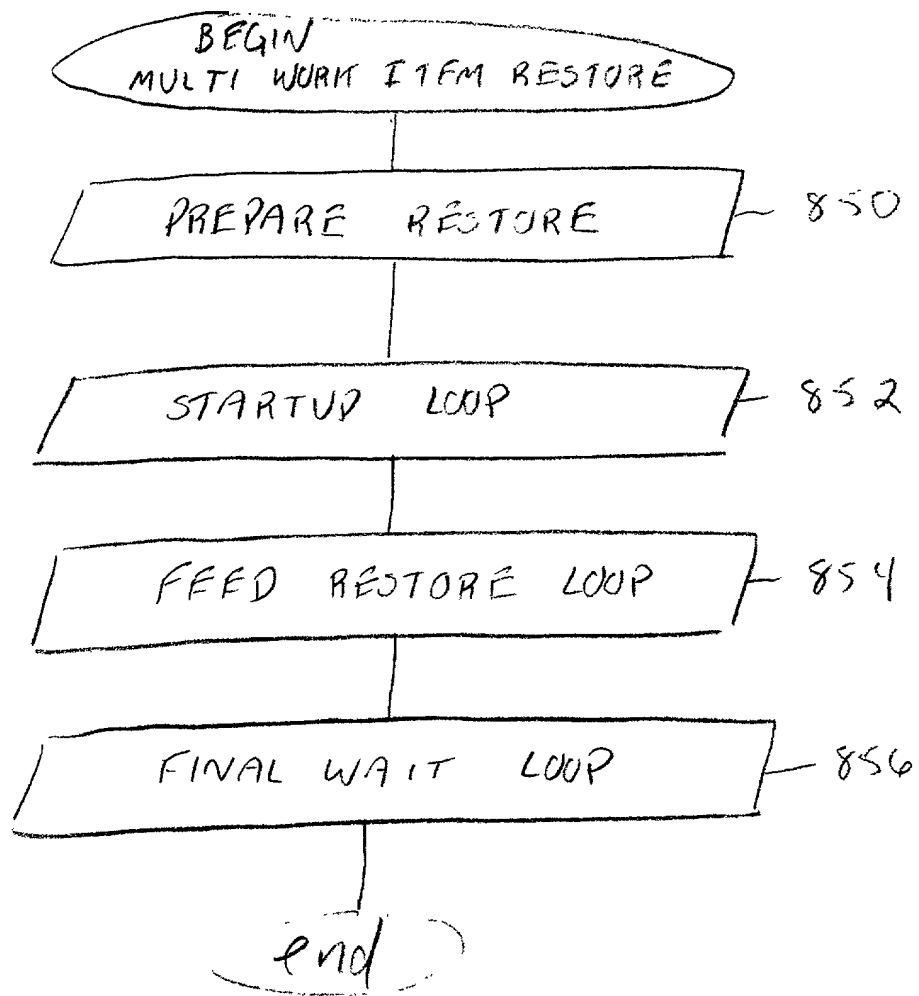


FIG. 16

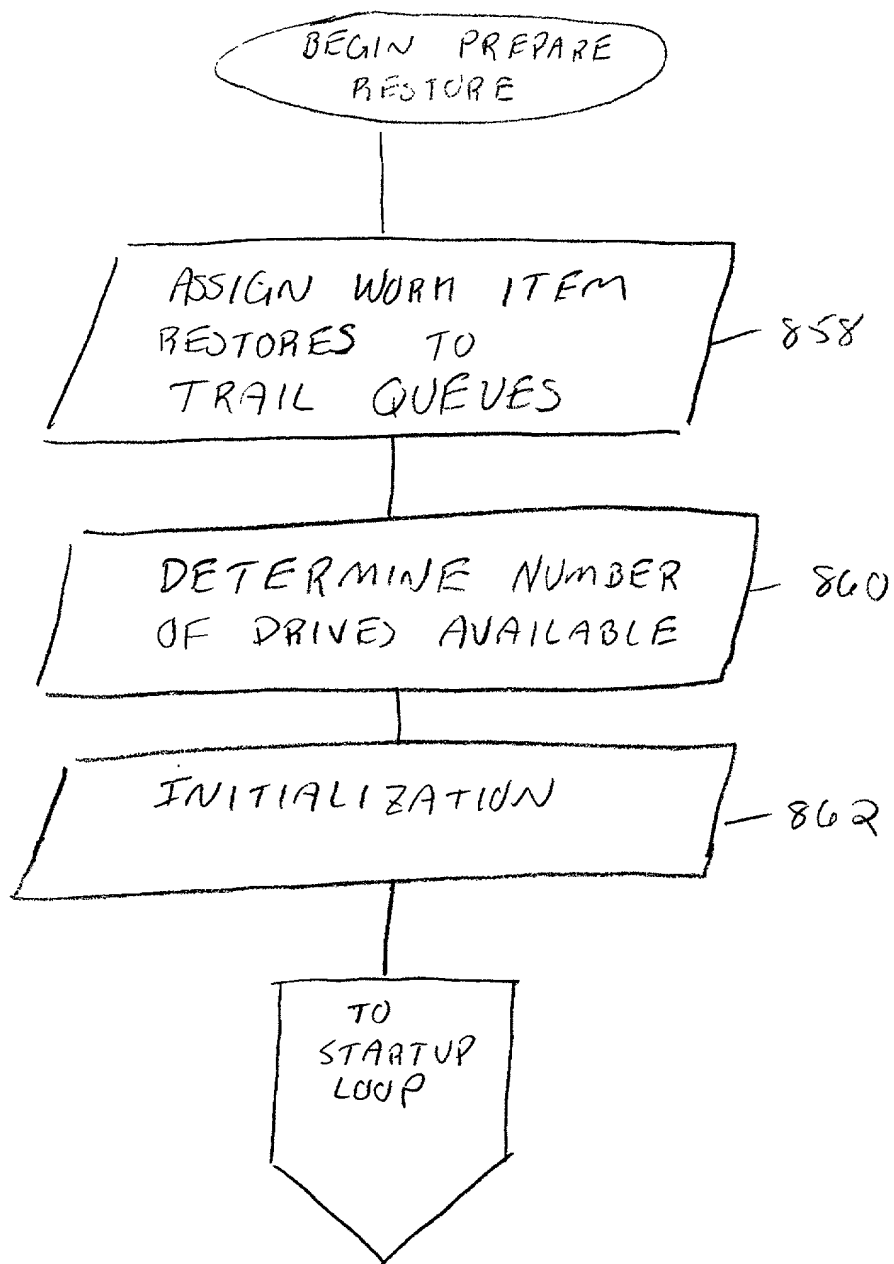


FIG 17

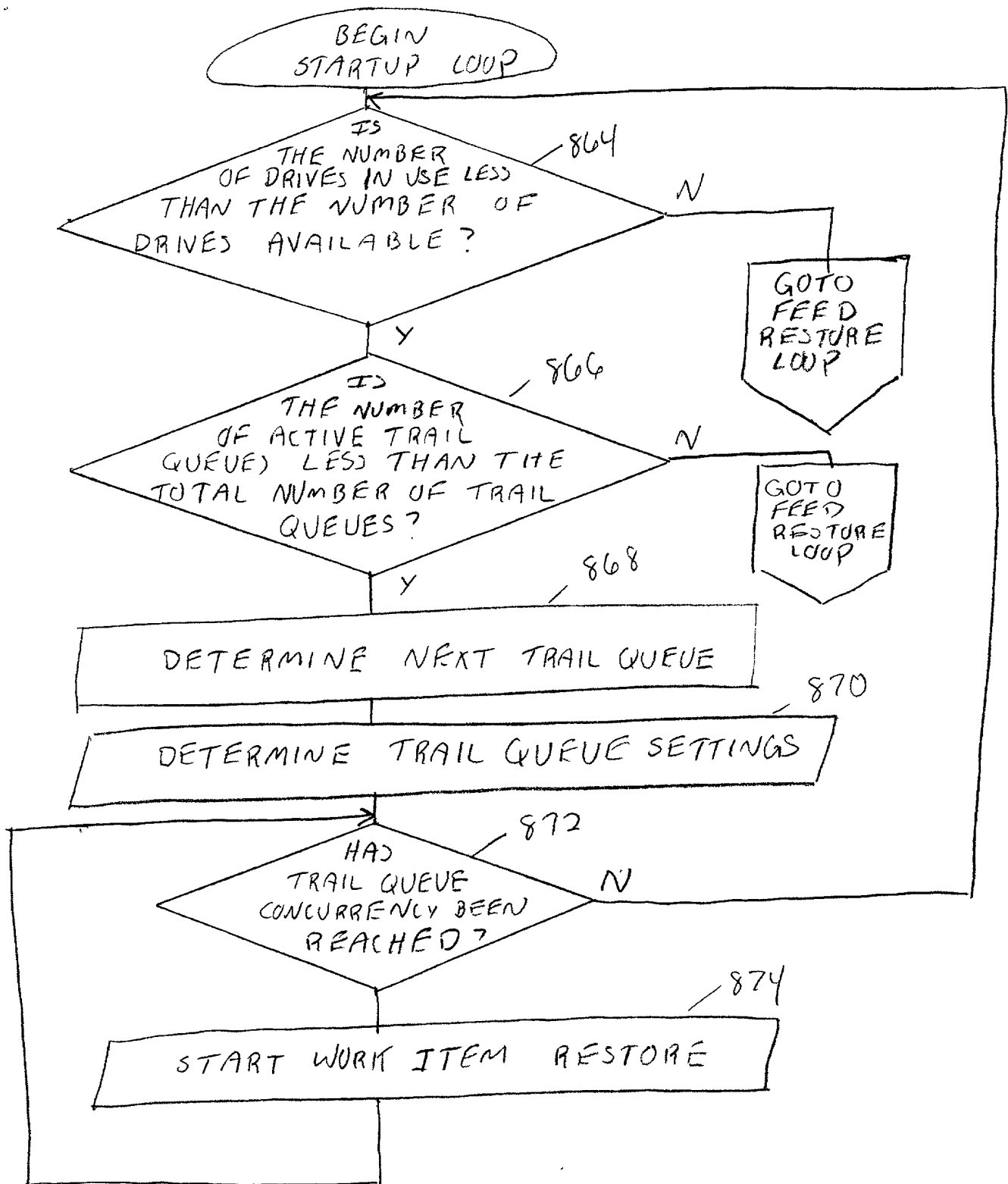


FIG 18

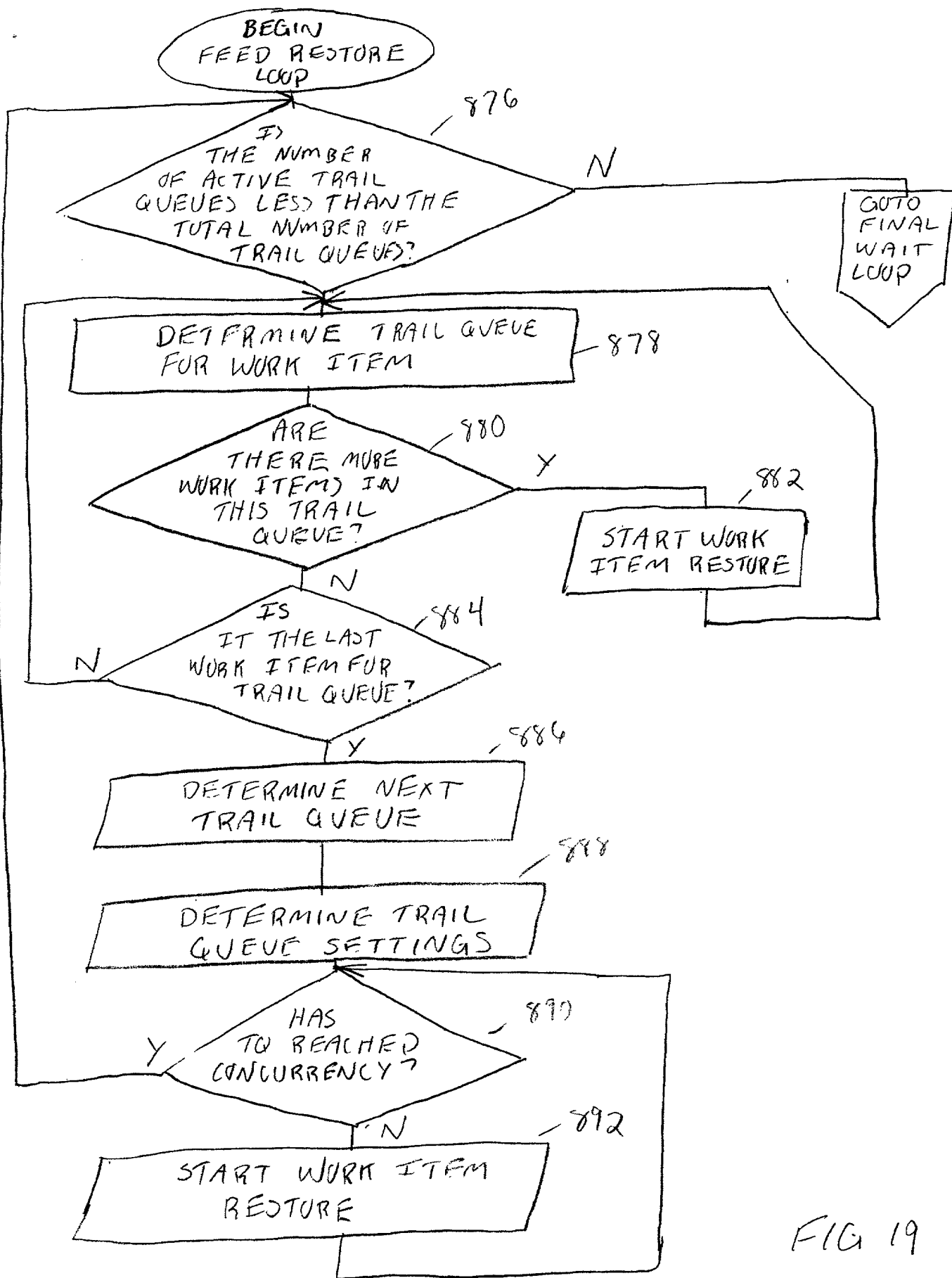


FIG 19

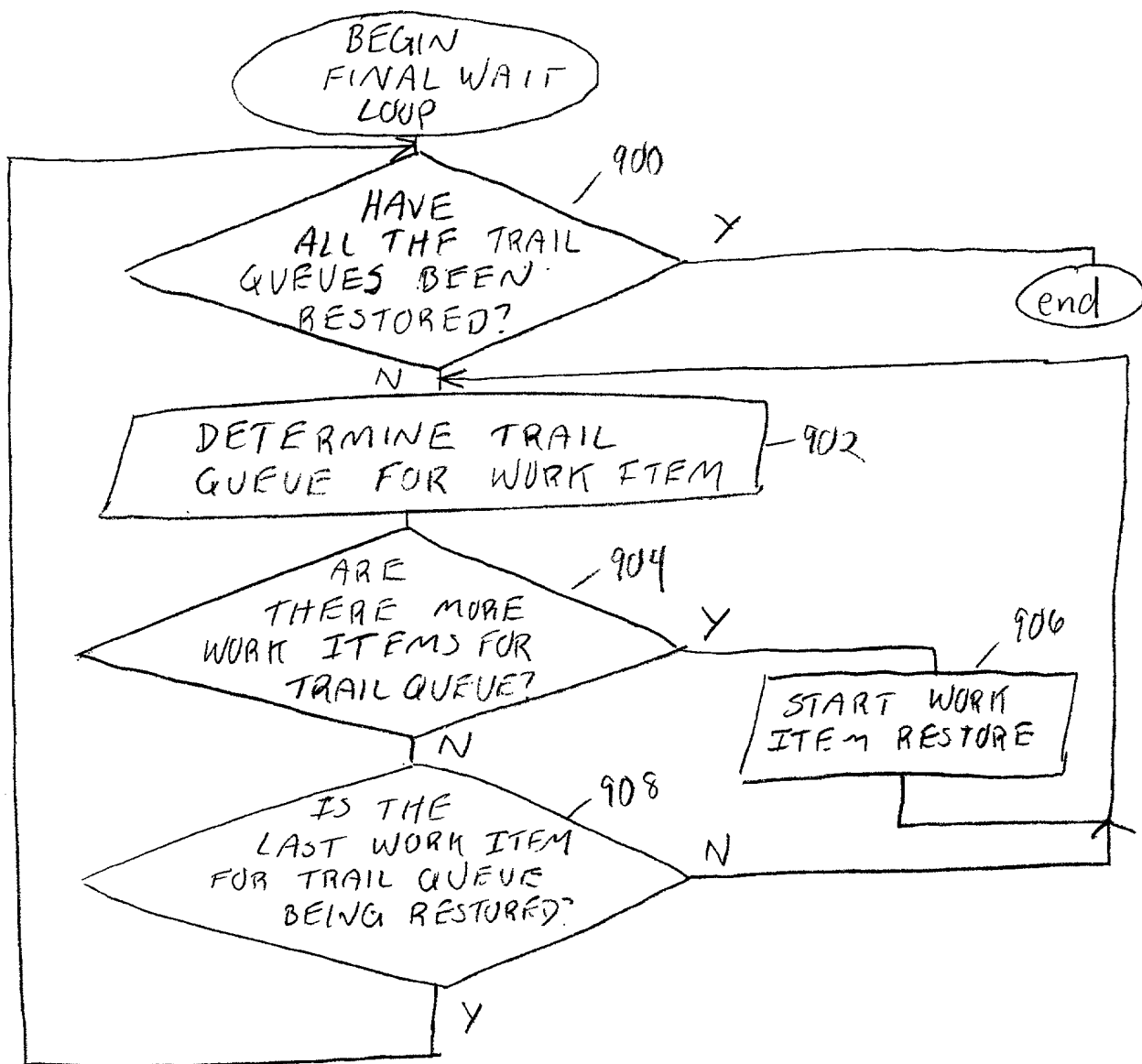


FIG 20